

EAST - [10694104.wsp.1]					File Edit Tools Window Help	
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Type	Hits	Search Text	DBs	Time Stamp		
1	BRS 9	((open or faulty) with solder) and (offset with (pad or ball or solder)))	EPO; JPO; DERWENT; IBM	2004/06/17		
2	BRS 9	((open or faulty) with solder) and (offset with (pad or ball or solder))) and (@ad<20031028 or @rlad<20031028	EPO; JPO; DERWENT; IBM	2004/06/17		
3	BRS 6	((open or faulty) with solder) and (offset with (pad or ball or solder))) and (@ad<20031028 or @rlad<20031028	USPAT; US-PGPUB	2004/06/17		
4	BRS 0	((open or faulty or defect or error) with (ball or pad or solder or joint) with (x\$1ray or (x adi ray))) and (@ad	USPAT; US-PGPUB	2004/06/17		
5	BRS 0	((open or faulty or defect or error) with (ball or pad or solder or joint) with (x\$1ray or (x adi ray))) and (@ad	USPAT; US-PGPUB	2004/06/17		
6	BRS 26	((open or faulty or defect or error) with (ball or pad or solder or joint) with (x\$1ray or (x adi ray))) and (@ad	USPAT; US-PGPUB	2004/06/17		
7	BRS 30	((open or faulty or defect or error) with (ball or pad or solder or joint) with (x\$1ray or (x adi ray))) and (@ad	USPAT; US-PGPUB	2004/06/17		
8	BRS 6	("4926452" and (@ad<20031028 or @rlad<20031028)) and (pad.clm.)	USPAT; US-PGPUB	2004/06/17		
9	BRS 4	((378/58.ccls.) and (@ad<20031028 or @rlad<20031028)) and (pad.clm.)	USPAT; US-PGPUB	2004/06/17		
10	BRS 2	((378/58.ccls.) and (@ad<20031028 or @rlad<20031028)) and (pad with offset)	USPAT; US-PGPUB	2004/06/17		
11	BRS 20	((open or faulty or defect or error) with (ball or pad or solder or joint) with (x\$1ray or (x adi ray))) and (@ad	USPAT; US-PGPUB	2004/06/17		
12	BRS 20	((378/58.ccls.) and (@ad<20031028 or @rlad<20031028)) and semiconductor	USPAT; US-PGPUB	2004/06/17		
13	BRS 13	"5012502" and (@ad<20031028 or @rlad<20031028)	USPAT; US-PGPUB	2004/06/17		
14	BRS 19	((open or faulty) with solder) and (offset with (pad or ball or solder))) and (@ad<20031028 or @rlad<20031028	USPAT; US-PGPUB	2004/06/17		
15	BRS 1	"5738269" PN.	USPAT	2004/06/17		
16	BRS 1	"5738269" PN.	USPAT	2004/06/17		
17	BRS 1	"5796590" PN.	USPAT	2004/06/17		
18	BRS 29	((open or faulty) with solder) and (offset with (pad or ball or solder))) and (@ad<20031028 or @rlad<20031028	USPAT; US-PGPUB	2004/06/17		
19	BRS 14	((open or faulty) with solder) and (offset with (pcb or (printed aid circuit) or printed\$1circuit))) and ((257/\$3.ccl	USPAT; US-PGPUB	2004/06/17		
20	BRS 13	((open or faulty) with solder) and (offset with (pcb or (printed aid circuit) or printed\$1circuit))) and ((257/\$3.ccl	USPAT; US-PGPUB	2004/06/17		
21	BRS 4	((open or faulty) with solder) and (offset with (pcb or (printed aid circuit) or printed\$1circuit))) and ((257/\$3.ccl	USPAT; US-PGPUB	2004/06/17		
22	BRS 9	((open or faulty) with solder) and (offset with (pcb or (printed aid circuit) or printed\$1circuit))) and ((257/\$3.ccl	USPAT; US-PGPUB	2004/06/17		
23	BRS 10	((open or faulty) with solder) and (offset with (pcb or (printed aid circuit) or printed\$1circuit))) and ((257/\$3.ccl	USPAT; US-PGPUB	2004/06/17		
24	BRS 6	((open or faulty) with solder) and (offset with (pcb or (printed aid circuit) or printed\$1circuit))) not (((open or	USPAT; US-PGPUB	2004/06/17		
25	BRS 0	(offset with (pcb or (printed aid circuit) or printed\$1circuit) with (solder or bump or ball) with "other") and (439/\$3.ccl	USPAT; US-PGPUB	2004/06/17		
26	BRS 1	(offset with (pcb or (printed aid circuit) or printed\$1circuit) with (solder or bump or ball) with relative) and (439/\$3.ccl	USPAT; US-PGPUB	2004/06/17		
27	BRS 33	(offset with (pcb or (printed aid circuit) or printed\$1circuit) with (solder or bump or ball) and (439/\$3.ccls.) a	USPAT; US-PGPUB	2004/06/17		
28	BRS 26	(offset with (pcb or (printed aid circuit) or printed\$1circuit) with (pads)) and (439/\$3.ccls.) and (@ad<20031028	USPAT; US-PGPUB	2004/06/17		
29	BRS 33	(offset with (pcb or (printed aid circuit) or printed\$1circuit) with (pads)) and (439/\$3.ccls.) and (@ad<20031028	USPAT; US-PGPUB	2004/06/17		
30	BRS 0	(offset with (pcb or (printed aid circuit) or printed\$1circuit) with (pads) with chip) and (439/\$3.ccls.) and (@a	USPAT; US-PGPUB	2004/06/17		
31	BRS 2	(offset with (pcb or (printed aid circuit) or printed\$1circuit) with (pads) with device) and (439/\$3.ccls.) and (@	USPAT; US-PGPUB	2004/06/17		
32	BRS 2	(offset with (board) with (pads) with device) and (439/\$3.ccls.) and (@ad<20031028 or @rlad<20031028)	USPAT; US-PGPUB	2004/06/17		
33	BRS 3	(offset with (board) with (pads) with device) and (257/\$3.ccls.) and (@ad<20031028 or @rlad<20031028)	USPAT; US-PGPUB	2004/06/17		
34	BRS 5	((open or faulty) with solder) and (offset with (pcb or (printed aid circuit) or printed\$1circuit)) and (439/\$3.ccls.	USPAT; US-PGPUB	2004/06/17		
35	BRS 0	((open or faulty) with (joint or ball or bump or solder)) and (offset near (pads)) and (257/\$3.ccls.) and (@ad	USPAT; US-PGPUB	2004/06/17		
36	BRS 25	((open or faulty or defect or error) with (ball or pad or solder or joint) with (x\$1ray or (x adi ray))) and (@a	USPAT; US-PGPUB	2004/06/17		
37	BRS 9	(offset with (pad) with pattern) and (257/\$3.ccls.) and (@ad<20031028 or @rlad<20031028)	USPAT; US-PGPUB	2004/06/17		
38	BRS 16	(offset with (pad) with pattern) and (257/\$3.ccls.) and (@ad<20031028 or @rlad<20031028)	USPAT; US-PGPUB	2004/06/17		
39	BRS 0	(offset with (pad) with pattern) and (257/\$3.ccls.) and (@ad<20031028 or @rlad<20031028) and (x\$1ray	USPAT; US-PGPUB	2004/06/17		
40	BRS 13	(offset with (pad) with pattern) and (438/\$3.ccls.) and (@ad<20031028 or @rlad<20031028)	USPAT; US-PGPUB	2004/06/17		
41	BRS 0	(offset with (pad) with pattern with intentionally) and (439/\$3.ccls.) and (@ad<20031028 or @rlad<20031028	USPAT; US-PGPUB	2004/06/17		
42	BRS 0	(offset with (pad) with pattern with intentionally) and (438/\$3.ccls.) and (@ad<20031028 or @rlad<20031028	USPAT; US-PGPUB	2004/06/17		
43	BRS 0	(offset with (pad) with pattern with intentionally) and (438/\$3.ccls.) and (@ad<20031028 or @rlad<20031028	USPAT; US-PGPUB	2004/06/17		

Type	Hits	Search Text	DBs	Time Stamp
BRS 14	19	(((((open or faulty) with solder) and (offset with (pcb or (printed adj circuit) or printed\$1circuit))) and ((257/\$3.ccl	USPAT: US-PGPUB	2004/06/17
BRS 13	20	(((((open or faulty) with solder) and (offset with (pcb or (printed adj circuit) or printed\$1circuit))) and ((257/\$3.ccl	USPAT: US-PGPUB	2004/06/17
BRS 4	21	(((((open or faulty) with solder) and (offset with (pcb or (printed adj circuit) or printed\$1circuit))) and ((257/\$3.c	USPAT: US-PGPUB	2004/06/17
BRS 9	22	((open or faulty) with solder) and (offset with (pcb or (printed adj circuit) or printed\$1circuit))	USPAT: US-PGPUB	2004/06/17
BRS 10	23	((open or faulty) with solder) and (offset with (pcb or (printed adj circuit) or printed\$1circuit))	USPAT: US-PGPUB	2004/06/17
BRS 6	24	(((((open or faulty) with solder) and (offset with (pcb or (printed adj circuit) or printed\$1circuit))) not ((((((open or	USPAT: US-PGPUB	2004/06/17
BRS 0	25	((offset with (pcb or (printed adj circuit) or printed\$1circuit) with (solder or bump or ball) with "other") and (439/USPAT: US-PGPUB	USPAT: US-PGPUB	2004/06/17
BRS 1	26	((offset with (pcb or (printed adj circuit) or printed\$1circuit) with (solder or bump or ball) with relative) and (439/USPAT: US-PGPUB	USPAT: US-PGPUB	2004/06/17
BRS 33	27	((offset with (pcb or (printed adj circuit) or printed\$1circuit) with (solder or bump or ball) and (439/\$3.ccls.) a	USPAT: US-PGPUB	2004/06/17
BRS 26	28	((offset with (pcb or (printed adj circuit) or printed\$1circuit) with (pads)) and (439/\$3.ccls.) and (@ad<20031	USPAT: US-PGPUB	2004/06/17
BRS 33	29	((offset with (pcb or (printed adj circuit) or printed\$1circuit) with (pads)) and (439/\$3.ccls.) and (@ad<200310	USPAT: US-PGPUB	2004/06/17
BRS 0	30	((offset with (pcb or (printed adj circuit) or printed\$1circuit) with (pads) with chip) and (439/\$3.ccls.) and (@a	USPAT: US-PGPUB	2004/06/17
BRS 2	31	((offset with (pcb or (printed adj circuit) or printed\$1circuit) with (pads) with device) and (439/\$3.ccls.) and (@	USPAT: US-PGPUB	2004/06/17
BRS 2	32	((offset with (board) with (pads) with device) and (439/\$3.ccls.) and (@ad<20031028 or @rlad<20031028)	USPAT: US-PGPUB	2004/06/17
BRS 3	33	((offset with (board) with (pads) with device) and (257/\$3.ccls.) and (@ad<20031028 or @rlad<20031028)	USPAT: US-PGPUB	2004/06/17
BRS 5	34	((open or faulty) with solder) and (offset with (pcb or (printed adj circuit) or printed\$1circuit)) and (439/\$3.ccls	USPAT: US-PGPUB	2004/06/17
BRS 0	35	((open or faulty) with (joint or ball or bump or solder)) and ((offset near (pads)) and (257/\$3.ccls.) and (@ad	USPAT: US-PGPUB	2004/06/17
BRS 25	36	(((((open or faulty or defect or error.) with (ball or pad or solder or joint) with (x\$1ray or (x adj ray))) and (@a	USPAT: US-PGPUB	2004/06/17
BRS 9	37	((offset with (pad) with pattern) and (257/\$3.ccls.) and (@ad<20031028 or @rlad<20031028)	USPAT: US-PGPUB	2004/06/17
BRS 16	38	((offset with (pad) with pattern) and (257/\$3.ccls.) and (@ad<20031028 or @rlad<20031028)	USPAT: US-PGPUB	2004/06/17
BRS 0	39	((offset with (pad) with pattern) and (257/\$3.ccls.) and (@ad<20031028 or @rlad<20031028)) and (x\$1ray	USPAT: US-PGPUB	2004/06/17
BRS 13	40	((offset with (pad) with pattern) and (438/\$3.ccls.) and (@ad<20031028 or @rlad<20031028)	USPAT: US-PGPUB	2004/06/17
BRS 0	41	((offset with (pad) with pattern with intentionally) and (439/\$3.ccls.) and (@ad<20031028 or @rlad<20031028	USPAT: US-PGPUB	2004/06/17
BRS 0	42	((offset with (pad) with pattern with intentionally) and (438/\$3.ccls.) and (@ad<20031028 or @rlad<20031028	USPAT: US-PGPUB	2004/06/17
BRS 0	43	((offset with (pad) with intentionally) and (438/\$3.ccls.) and (@ad<20031028 or @rlad<20031028)	USPAT: US-PGPUB	2004/06/17
BRS 1	44	((offset with (pad) with intentionally) and (257/\$3.ccls.) and (@ad<20031028 or @rlad<20031028)	USPAT: US-PGPUB	2004/06/20
BRS 32	45	((offset with intentionally) and (257/\$3.ccls.) and (@ad<20031028 or @rlad<20031028)	USPAT: US-PGPUB	2004/06/20
BRS 23	46	(((pcb with pad) and (257/\$3.ccls.) and (@ad<20031028 or @rlad<20031028)) and (solder or bump)) and (p	USPAT: US-PGPUB	2004/06/20
BRS 20	47	(((pcb with pad) and (257/\$3.ccls.) and (@ad<20031028 or @rlad<20031028)) and (solder or bump)) and (USPAT: US-PGPUB	2004/06/20
BRS 3	48	((offset with (pad near pattern)) and semiconductor and (@ad<20031028 or @rlad<20031028) and (solder o	USPAT: US-PGPUB	2004/06/20
BRS 6	49	(((offset near layout) and (257/\$3.ccls.) and (@ad<20031028 or @rlad<20031028)) and (solder or bump)	USPAT: US-PGPUB	2004/06/20
BRS 26	50	((("four" near2 pad) and ((257/\$3.ccls.) or (438/\$3.ccls))) and (@ad<20031028 or @rlad<20031028)) and (@p	USPAT: US-PGPUB	2004/06/20
BRS 13	51	((((group or grouping) adj "four") with pad) and ((257/\$3.ccls.) or (438/\$3.ccls))) and (@ad<20031028 or @rlad<20031028)) and (@p	USPAT: US-PGPUB	2004/06/20
BRS 0	52	((((group or grouping) adj "four") with pad) and ((174/\$3.ccls))) and (@ad<20031028 or @rlad<20031028)) and (@p	USPAT: US-PGPUB	2004/06/20
BRS 32	53	((((group or grouping) adj "four") with pad) and semiconductor) and (@ad<20031028 or @rlad<20031028)) and (@p	USPAT: US-PGPUB	2004/06/20
BRS 22	54	((((group or grouping) adj "four") with pad) and semiconductor) not (((((group or grouping) adj "four") with pad)	USPAT: US-PGPUB	2004/06/20
BRS 16	55	((US-6742247-\$ or US-6380060-\$ or US-6163462-\$ or US-6169329-\$ or US-5477082-\$ or US-6548392-\$ or U	USPAT: US-PGPUB	2004/06/24
BRS 1	56	((US-6742247-\$ or US-6380060-\$ or US-6163462-\$ or US-6169329-\$ or US-5477082-\$ or US-6548392-\$ or	USPAT: US-PGPUB	2004/06/24
BRS 0	57	((method with (inspection or detection or fault) with (ball or pad or solder or joint) with (error or open or defect o	USPAT: US-PGPUB	2004/06/24
BRS 1	58	((method with (inspection or detection or fault) with (ball or pad or solder or joint) with (error or open or defect	USPAT: US-PGPUB	2004/06/24
BRS 5	59	((method with (inspection or detection or fault) with (ball or pad or solder or joint) with (error or open or defect	USPAT: US-PGPUB	2004/06/24
BRS 8	60	(((((open or faulty or defect or error.) with (ball or pad or solder or joint) with (x\$1ray or (x adj ray))) and (@ad	USPAT: US-PGPUB	2004/06/24

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